Self-Encrypting Deception: weaknesses in the encryption of solid state drives (SSDs)

Carlo Meijer Radboud University Nijmegen Midnight Blue Labs







whoami

Carlo Meijer

- PhD Student at Radboud University Nijmegen
- Focused on analysis of crypto systems deployed in the wild
- Known for
 - New cryptanalytic attack on Mifare Classic (2015)
 - · Password generators in wireless routers (2015)
 - · Self-Encrypting Drives (2018)
- Independent security researcher at Midnight Blue Labs
- 🖻 c.meijer@cs.ru.nl
- https://cs.ru.nl/~cmeijer/
- https://midnightbluelabs.com/









Acknowledgements

Philipp Gühring

For the bulk of the Samsung 840 EVO reverse engineering work

See http://www.futureware.at/~philipp/ssd/TheMissingManual.pdf If you like reversing, check out his projects at

http://www.futureware.at/~philipp/ssd/





























Samsung 840 EVO mSATA SSD Specifications:

- Max capacity: 1TB
- Memory: 1GB LPDDDR2 DRAM
- Controller: Samsung MEX (3x ARM Cortex R4 cores @400MHz)
- NAND: 19nm Samsung TLC
- Interface: SATA
- Form Factor: mSATA
- Power Consumption
 - Start-up: 2.01W
 - Idle: 0.44W
- Dimensions Height x length x Thickness: 3cm x 5cm x 3.85mm
- Weight: 8.5 grams
- Warranty: 3 year limited













supports high-speed NAND flash interfaces up to 200 e Marvell 88FR102 V5 CPU with shared DTCM and ITCM sh channels, ~500MBps sequential write performance,



https://www.storagereview.com/samsung_840_evo_msata_ssd_review









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The best way to enhance data security: Swap out vulnerable hard drives for self-encrypting SSDs

The best way to protect data stored on servers, desktops, or laptops is to encrypt it at the hardware level on a device's storage drive. This is just one of many standard data security steps, but it's critical – and often overlooked. The reason: New systems often come with low-grade, preinstalled hard drives, which often lack encryption technology. Or, if the hard drive offers encryption, it's typically software-based, which is one of the weakest forms of encryption and may severely slow system performance, plus it's also easier for hackers to attack. Here's why.



https://www.crucial.com/usa/en/how-self-encrypting-ssds-protect-your-business-and-enhance-data-security-and-limit-liability







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Hardware based encryption is very secure; far more secure than any software-based offering. Software can be corrupted or negated, while hardware cannot,

Software runs under an operating system that is vulnerable to viruses and other attacks. An operating system, by definition, provides open access to applications and thus exposes these access points to improper use.

Hardware based security can more effectively restrict access from the outside, especially to unauthorized use. Additionally, dedicated hardware can have superior performance compared to software

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- Transparency: No system or application modifications required; encryption key
 generated in the factory by on-drive random number process; drive is always encrypting
- Ease of management: No encryption key to manage; software vendors exploit standardized interface to manage SEDs, including remote management, pre-boot authentication, and password recovery
- Disposal or re-purposing cost: With an SED, erase on-board encryption key
- Re-encryption: With SED, there is no need to ever re-encrypt the data
- · Performance: No degradation in SED performance; hardware-based
- Standardization: Whole drive industry is building to the TCG/SED Specifications
- · Simplified: No interference with upstream processes



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BitLocker (built into Windows) opts for hardware encryption **by default** if available, software as a fall-back







Security guarantees

of Self-Encrypting Drives





Typical three attacker models for Full-Disk Encryption



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Overall: Attack opportunities are more or less equivalent





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Overall: SEDs don't offer added protection \rightarrow equivalent





Security guarantees of Self-Encrypting Drives

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- Extremely hard to audit
- Additional pitfalls that apply particularly to hardware encryption (later)





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Thus, security guarantees are equivalent. At best.





Standards

for Self-Encrypting Drives





Standards for Self-Encrypting Drives

Two widely used standards exist

(i) ATA Security Feature Set

Originally designed for access control only



https://medium.com/@andrewpgsweeny/ beyond-the-red-pill-and-the-blue-pill-9ef953d6e133







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Modern standard designed specifically for SEDs



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Suppose you would implement this yourself



It would probably look something like this







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So far, easy







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Thus, "encryption" is not even mentioned in the spec



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- Two password types: User, Master





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 - Maximum: Only User unlocks drive, Master may erase
- Bottom line: Always change the Master password or set to Maximum





















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- Multiple partitions (*locking ranges*)







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- Fully trusted by BitLocker







Pitfalls















· Password unlocks drive and DEK is used to encrypt data









- · Password unlocks drive and DEK is used to encrypt data
- How they are linked is unknown









- · Password unlocks drive and DEK is used to encrypt data
- How they are linked is unknown
- They might not be linked at all















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Even to ranges for which no permission is granted







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 \rightarrow Thus, in this case, DEK is retrievable **without** a key





Pitfall 3: Lack of random entropy

DEK may appear random, but how was it generated?



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DEK may appear random, but how was it generated?

• Embedded devices have a notoriously bad reputation





Multiple writes to the *same* logical sector trigger writes to *different* physical sectors







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- Set password \rightarrow overwrite of unprotected DEK with encrypted variant





Multiple writes to the *same* logical sector trigger writes to *different* physical sectors



- Set password \rightarrow overwrite of unprotected DEK with encrypted variant
- Unprotected DEK may still be present in physical flash







PC sends DEVSLP signal to drive when idle



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Drive goes into power-saving mode









- Drive goes into power-saving mode
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- Erasing on resume is crucial





Everything that applies to software encryption still applies



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Everything that applies to software encryption still applies



• Mode of operation (ECB, CBC, CTR, XTS)







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- etc.











General approach







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(i) Obtain a firmware image







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- (ii) Gain low level control over the device







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- (iii) Analyze the firmware




Methodology

General approach

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(i) Download it (harder than it seems)







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dword_10222A58 = sub_1003E390(); v131 = 0; v130 = 1; v129 = 0; *(BYTE *)sub 1002D920(v1, v0, &v129) = 77; // M v129 = 1; *(BYTE *)sub 1002D920(v3, v2, &v129) = 54; v129 = 2; *(BYTE *)sub 1002D920(v5, v4, &v129) = 97; // a v129 = 3; *(BYTE *)sub 1002D920(v7, v6, &v129) = 56; v129 = 4:*(BYTE *)sub 1002D920(v9, v8, &v129) = 103; // g v129 = 5:*(BYTE *)sub 1002D920(v11, v10, &v129) = 51: v129 = 6:*(_BYTE *)sub_1002D920(v13, v12, &v129) = 105;// i v129 = 7:*(_BYTE *)sub_1002D920(v15, v14, &v129) = 37; v129 = 8: *(_BYTE *)sub_1002D920(v17, v16, &v129) = 99; // c v129 = 9; *(_BYTE *)sub_1002D920(v19, v18, &v129) = 50; v129 = 10; *(_BYTE *)sub_1002D920(v21, v20, &v129) = 105;// i v129 = 11; *(_BYTE *)sub_1002D920(v23, v22, &v129) = 33; v129 = 12; *(_BYTE *)sub_1002D920(v25, v24, &v129) = 97; // a v129 = 13; *(BYTE *)sub_1002D920(v27, v26, &v129) = 122; v129 = 14;*(BYTE *)sub 1002D920(v29, v28, &v129) = 110;// n v129 = 15;

Decompilation of Samsung Magician tool





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 - Image may be encrypted, decryption by the unit itself → dead end
- (ii) Pull the firmware from RAM through JTAG (next)

dword_10222A58 = sub_1003E390(); v131 = 0; v130 = 1; v129 = 0; *(_BYTE *)sub_1002D920(v1, v0, &v129) = 77; // M v129 = 1; *(BYTE *)sub 1002D920(v3, v2, &v129) = 54; v129 = 2; *(BYTE *)sub 1002D920(v5, v4, &v129) = 97; // a v129 = 3;*(BYTE *)sub 1002D920(v7, v6, &v129) = 56; v129 = 4:*(BYTE *)sub 1002D920(v9, v8, &v129) = 103; // g v129 = 5: *(BYTE *)sub 1002D920(v11, v10, &v129) = 51; v129 = 6: *(_BYTE *)sub_1002D920(v13, v12, &v129) = 105;// i v129 = 7:*(_BYTE *)sub_1002D920(v15, v14, &v129) = 37; v129 = 8: *(_BYTE *)sub_1002D920(v17, v16, &v129) = 99; // c v129 = 9; *(_BYTE *)sub_1002D920(v19, v18, &v129) = 50; v129 = 10; *(_BYTE *)sub_1002D920(v21, v20, &v129) = 105;// i v129 = 11; *(_BYTE *)sub_1002D920(v23, v22, &v129) = 33; v129 = 12;*(_BYTE *)sub_1002D920(v25, v24, &v129) = 97; // a v129 = 13;*(_BYTE *)sub_1002D920(v27, v26, &v129) = 122; v129 = 14;*(BYTE *)sub 1002D920(v29, v28, &v129) = 110;// n v129 = 15;

Decompilation of Samsung Magician tool







Methodology

General approach

- (i) Obtain a firmware image
- (ii) Gain low level control over the device
- (iii) Analyze the firmware







More or less equal capabilities:

(i) JTAG (allows you to halt the CPU, get/set registers, read/write in the address space, etc.)







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JTAG pins on the Crucial MX100.





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 - Bypass cryptographic signatures with fault injection



JTAG pins on the Crucial MX100.









Methodology

General approach

- (i) Obtain a firmware image
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(i) Figure out the section information







@pinacolada:~/Documents/ssdproject/crucial\$ php parse fw.php firmware mx300/M0CR060.bin +1 found MCRN header -- B8KB [segment] [type] [source] [dest] [size] 0 0 0x00000010 0x00000000 117456 0 0x0001cae0 0x0001fa00 352 0 0x0001cc40 0x04002100 2488 0 0x0001d5f8 0x80001000 240 0 0x08000000 0x80880000 16 5 0 0x0001d6e8 0x80041000 264 0 0x0001d7f0 0x801c4000 1035224 6 255 255 0xffffffff 0xffffffff 4294967295 [*1 new offset : 0x11aa00 [*] new offset : 0x133c00 [*] new offset : 0xfa540c00 ser@pinacolada:~/Documents/ssdproject/crucial\$

Parsed header of MX300 FW image

- (i) Figure out the section information
 - · From image header





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Parsed header of MX300 FW image

- (i) Figure out the section information
 - From image header
- (ii) Load the image into a disassembler

(We used IDA Pro for this purpose)







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Try to find the ATA dispatch table

Atacommana	<ux93,< td=""><td>SUD_80262F58,</td><td>UX454AUUU3></td></ux93,<>	SUD_80262F58,	UX454AUUU3>
AtaCommand	<0x45,	sub_80264DC0,	0x45DA0023>
AtaCommand	<0xF1,	sub_8022CA10,	0x47CB0000>
AtaCommand	<0xF2,	sub_8022CAE8,	0x7890000>
AtaCommand	<0xF3,	sub_8022C76C,	0x67C90000>
AtaCommand	<0xF4,	sub_8022C7F4,	0x67C90002>
AtaCommand	<0xF5,	sub_8022C98C,	0x7CA0000>
AtaCommand	<0xF6,	sub 8022C6C4,	0x47CB0000>
AtaCommand	<0xB0,	AtaSmart, 0x4	880003>
AtaCommand	<0x10,	sub_80264CD0,	0x4CA0000>
AtaCommand	<0x78,	sub_801C6B00,	0x45CA0020>
AtaCommand	<0xB4,	sub_801C9D60,	0x2E880023>
AtaCommand	<6, sub	_801CBB74, 0x	65DA0023>
AtaCommand	<0xE7,	sub_801CAF14,	0x45DA0000>
AtaCommand	<0xEA,	sub_801CAF14,	0x45DA0022>
AtaCommand	<0xEF,	sub_80264780,	0x5C80000>
AtaCommand	<0xC6,	sub_801CB3A8,	0x5C80000>
AtaCommand	<0xEC.	sub 802640C8	0x4080000>
			~

ATA Dispatch table in firmware

Command feature set			
Retired	11h1Fh, 71h7Fh, 94h.		
Sanitize Device	B4h	0	
SECURITY DISABLE PASSWORD	F6h	0	
SECURITY ERASE PREPARE	F3h	0	
SECURITY ERASE UNIT	F4h	0	
SECURITY FREEZE LOCK	F5h	0	
SECURITY SET PASSWORD	F1h	0	
SECURITY UNLOCK	F2h	0	
SET FEATURES	EFh	м	
SET MAX ADDRESS	F9h	0	
SET MAX ADDRESS EXT	37h	0	
SET MULTIPLE MODE	C6h	0	
SLEEP	E6h	м	
SMART	B0h	0	
STANDBY	E2h	M	
STANDBY IMMEDIATE	E0h	M	
TRUSTED NON-DATA	5Bh	0	
TRUSTED RECEIVE	5Ch	0	

ATA specification





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- (iii) Figure out what the firmware does
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 - Look through functions with interesting opcodes

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AtaCommand	<0x80	AtaSmart, 0x4	880003>
AtaCommand	<0x10	sub 80264CD0	0x4CA0000>
AtaCommand	<0x78	sub 801C6800	0x45CA0020>
AtaCommand	<0xR4	sub 801C9060	0x2E880023>
AtaCommand	<6 sub	801CBB74 0x	55DA0023>
AtaCommand	-0-57	aub BOACATAA	0-45040000-
Atacommanu	COXE/,	SUD_OUICAFI4,	0X45DA00002
AtaCommand -	<0xEA,	sub_801CAF14,	0x45DA0022>
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ATA specification







Case studies







• Marvell 88SS9189 controller









- Marvell 88SS9189 controller
- Dual-core 88FR102 V5 (ARM)









- Marvell 88SS9189 controller
- Dual-core 88FR102 V5 (ARM)
- Hardware crypto co-processor







Firmware images

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Firmware images

• Bootable ISO image

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Firmware images

- Bootable ISO image
- Cryptographically signed (RSA)
- Has a JTAG debugging interface



JTAG pinout on a Crucial MX100







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• Successor to MX100









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- JTAG pins moved









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- Same controller, similar firmware









- Successor to MX100
- JTAG pins moved
- Same controller, similar firmware
- Same vulnerabilities





Successor to MX200









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- Move to TLC memory, newer controller









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Differences

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Code execution?













Crucial MX300 boot process.

(i) Boot code in ROM











Crucial MX300 boot process.

- (i) Boot code in ROM
- (ii) Stage 2 in SPI flash











Crucial MX300 boot process.

- (i) Boot code in ROM
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 \rightarrow Modify Stage 2









Crucial MX300 boot process.

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→ Modify Stage 2



Midnight Blue





Code execution walkthrough

(i) Connect reader to SPI flash chip









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Code execution walkthrough

- (i) Connect reader to SPI flash chip
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Drive now accepts fw updates with invalid signatures







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- (ii) Make backup
- (iii) Craft code that removes signature checks from fw
- (iv) Inject it between firmware retrieval and transfering control to it
- (v) Flash modified Stage 2
 Drive now accepts fw updates with invalid signatures
- (vi) Take a firmware image and add additional "features", such as arbitrary read/write/execute capabilities
- (vii) Send the modified firmware as you would for any update Now we have full control over the device









Key derivation scheme

• Binding between password and DEK introduced









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Key derivation scheme

- Binding between password and DEK introduced
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- RDS-key allows access to all protected ranges

Prevented by firmware, though not cryptographically enforced



Scheme used to obtain a range key (DEK) from the user-supplied password. In this example, credential #2 is used to unlock range #3.

Thus, everything is accessible with a single valid password. But it's even worse (next)







Consider the password protection function

int __fastcall ProtectPwd_Impl(unsigned __int8 *lpPassword, int dwPasswordLength, int bStoreRdsKey, unsigned __int8 *lpOutput) int dwPasswordLength : // r8 unsigned __int8 *lpPassword_; // r9 unsigned __int8 *lpOutput_; // r7 int bStoreRdsKev ; // r4 ProtectedPassword stProtectedPassword; // [sp+Ch] [bp-C4h] unsigned __int8 abRandomSalt[32]; // [sp+54h] [bp-7Ch] unsigned int8 abRdsKeyInput[32]; // [sp+74h] [bp-5Ch] unsigned __int8 abHmacKey[32]; // [sp+94h] [bp-3Ch] dwPasswordLength_ = dwPasswordLength; lpPassword = lpPassword: lpOutput = lpOutput; bStoreRdsKey_ = bStoreRdsKey; bzero(abRdsKevInput, 32); GenerateRandom(abRandomSalt, 32); PBKDF2 HMAC SHA256(lpPassword , dwPasswordLength , abRandomSalt, 32, 100, 32, abHmacKev); if (!bStoreRdsKey_) goto LABEL 4; if (IsRdsKeyValid()) memcpv(abRdsKevInput, g abRdsKev, 32); LABEL_4: AesEncrypt(abRdsKeyInput, 32, abHmacKey, stProtectedPassword,abCipherText); memcpy(stProtectedPassword.abSalt, abRandomSalt, 32); return AesEncrypt(stProtectedPassword.abCipherText, 72, g_abDeviceKey, lpOutput_); return printf_0("!! Protect Pwd: RDSKEY is not valid !!\n"); 3

• "Protect Pwd" does more than just password hashing





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- · "Protect Pwd" does more than just password hashing
- Output contains encrypted RDS-key
- They shouldn't be throwing its output around







Consider this trace captured during BitLocker provisioning

VerifyPasswd(szPasswd="AEGIS_ACADIA_MSID_12456789012345", bExtractRdsKey=true, dwSlotNo=2) VerifyPasswd(szPasswd="AEGIS ACADIA MSID 12456789012345", bExtractRdsKey=true, dwSlotNo=2) CopyCredential(dwSourceSlot=2, dwDestinationSlot=10) ProtectPasswd(szPasswd=[0x00 × 32], bStoreRdsKey=true, dwSlotNo=11) szPasswd is zero huffer CopyCredential(dwSourceSlot=11, dwDestinationSlot=12) CopyCredential(dwSourceSlot=11, dwDestinationSlot=13) CopyCredential(dwSourceSlot=11, dwDestinationSlot=14) CopyCredential(dwSourceSlot=11, dwDestinationSlot=29) StoreCryptoContextInSpiFlash() VerifyPasswd(szPasswd="AEGIS_ACADIA_MSID_12456789012345", bExtractRdsKey=true, dwSlotNo=2) VerifyPasswd(szPasswd="AEGIS_ACADIA_MSID_12456789012345", bExtractRdsKev=true, dwSlotNo=10) VerifyPasswd(szPasswd="AEGIS_ACADIA_MSID_12456789012345", bExtractRdsKey=true, dwSlotNo=2) ProtectPasswd(szPasswd=«BitLocker SID password», bStoreRdsKev=true, dwSlotNo=2)) StoreCryptoContextInSpiFlash() VerifyPasswd(szPasswd="AEGIS_ACADIA_MSID_12456789012345", bExtractRdsKey=true, dwSlotNo=10) ProtectPasswd(szPasswd=«BitLocker SID password», bStoreRdsKev=true, dwSlotNo=10) StoreCryptoContextInSpiFlash() VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKey=true, dwSlotNo=15) GenerateRandomDekAndWrap(dwRangeNo=1, blsProtectedRange=false) VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKev=true, dwSlotNo=15) StoreCryptoContextInSpiFlash() UnwrapDek(dwRangeNo=1, blsProtectedRange=false) VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKey=true, dwSlotNo=15) VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKey=true, dwSlotNo=15) VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKev=true, dwSlotNo=15) UnwrapDek(dwRangeNo=1, blsProtectedRange=false) WrapDek(dwRangeNo=1, blsProtectedRange=true) VerifyPasswd(szPasswd=[0x00 × 32], bExtractRdsKev=true, dwSlotNo=15) ProtectPasswd(szPasswd=«BitLocker user password», bStoreRdsKey=true, dwSlotNo=15) StoreCryptoContextInSpiElash() VerifyPasswd(szPasswd=«BitLocker user password», bExtractRdsKev=true, dwSlotNo=15) VerifyPasswd(szPasswd=«BitLocker user password», bExtractRdsKey=true, dwSlotNo=15)






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VentyPasswd(szPasswd=«BitLocker user password», bExtractRdsRey=true, dwSlotNo=15) VerifyPasswd(szPasswd=«BitLocker user password», bExtractRdsRey=true, dwSlotNo=15) • RDS key ends up in all slots 11-29 (except 15)







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VerifyPasswd(szPasswd=«BitLocker user password», bExtractRdsKey=true, dwSlotNo=15)

- RDS key ends up in all slots 11-29 (except 15)
- Decryption key is a zero
 buffer







Attack strategy

(i) Flash modified firmware image (before)





Attack strategy

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- (ii) Craft code that recovers RDS key from credential slot 11 (using zero buffer)







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RDS key is now recovered







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- (iii) Execute the code on the drive RDS key is now recovered
- (iv) Modify the password verification routine so that it accepts any password
- (v) Unlock any desired range with an arbitrary password





Demo







Get best-in-class hardware encryption.

Keep personal files and sensitive information secure from hackers and thieves with AES 256-bit encryptionthe same grade used by banks and hospitals. The Crucial MX100 is one of the only drives available that meets Microsoft® eDrive®, IEEE-1667, and TCG Opal 2.0 standards of encryption.

https://www.crucial.com/wcsstore/CrucialSAS/pdf/product-flyer/ssd/productflyer-crucial-mx100-ssd-en.pdf







ATA Security

• The crucial MX300 has an empty ("") factory-set master password







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ATA Security

- The crucial MX300 has an empty ("") factory-set master password
- Recall: *change* the Master password or set MASTER PASSWORD CAPABILITY to *Maximum* (1)
- For the MX300, latter is insufficient
- Can use the arbitrary write to patch it back to High (0), unlock with empty string









• First Samsung drive to support TCG Opal









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- Tri-core Cortex-R4, 400 Mhz







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From Opal password to DEK







From Opal password to DEK



• Sound Opal implementation





From Opal password to DEK



- Sound Opal implementation
- All properties cryptographically enforced





ATA Security feature set



iCIS | Digital Security Radboud University



ATA Security feature set

• DEK depends on password **only** in *Maximum* mode







ATA Security feature set

- DEK depends on password **only** in *Maximum* mode
- Otherwise, **no dependency** on password whatsoever Removal of hash comparison allows access







Crypto data structure storage



iCIS | Digital Security Radboud University



Crypto data structure storage



· All crypto related data is stored in NAND, wear leveled





Crypto data structure storage



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- Thus, can scan through NAND for unprotected keys





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- If found, completely compromises encryption







Crypto data structure storage



- All crypto related data is stored in NAND, wear leveled
- Thus, can scan through NAND for unprotected keys
- If found, completely compromises encryption
- Affects both ATA security and TCG Opal









Successor to 840 EVO









- Successor to 840 EVO
- Samsung MGX controller









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- Different firmware obfuscation
 de-obfuscation still performed on the host pc









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- Successor to 840 EVO
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- Different firmware obfuscation
 de-obfuscation still performed on the host pc
- Supports DEVSLP
- Very similar encryption implementation
- Thus, **same vulnerability**, except the wear-leveling issue





Samsung T3 Portable

• Essentially a 850 EVO with USB↔mSATA adapter (with T3-specific firmware, not available for download)








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Configuration tool for Windows, MacOS and Android







- Essentially a 850 EVO with USB↔mSATA adapter (with T3-specific firmware, not available for download)
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Proprietary security command set





- Configuration tool for Windows, MacOS and Android
- Built on the ATA security implementation

With master password capability set to High





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Proprietary security command set





- Configuration tool for Windows, MacOS and Android
- Built on the ATA security implementation

With master password capability set to ${\bf High}$

Thus, password and DEK not linked, equivalent to no encryption





Demo





Tough to Crack

A trifecta of protection, the shockresistant T3 has a strong exterior metal body, internal support frame and optional AES 256-bit hardware encryption prepared for demands of life.





https://www.samsung.com/semiconductor/minisite/ssd/product/portable/t3/







Key differences with T3

• USB 3.1 Gen2 instead of Gen1









Key differences with T3

- USB 3.1 Gen2 instead of Gen1
- JTAG switched off









Key differences with T3

- USB 3.1 Gen2 instead of Gen1
- JTAG switched off
- Equally vulnerable

Just harder to exploit











Most SEDs have severe • weaknesses

Drive	1	2	3	4	5	6	7	8	9	Impact
Crucial MX100	×	×	×							Compromised
(all form factors)										
Crucial MX200	×	×	×							Compromised
(all form factors)										
Crucial MX300	\checkmark	1	1		×	\checkmark	1	1	1	Compromised
(all form factors)										
Samsung 840	×	1	1		1	\checkmark	1	×	1	~ Depends
EVO (SATA)										
Samsung 850	×	1	1		1	\checkmark	1	1	1	~ Depends
EVO (SATA)										
Samsung T3				×						Compromised
(USB)										
Samsung T5				×						Compromised
(USB)										

¹ Cryptographic binding in ATA Security (High mode) ² Cryptographic binding in ATA Security (Max mode)

³ Cryptographic binding in TCG Opal

⁴ Cryptographic binding in proprietary standard

⁵ No single key for entire disk

⁶ Randomized DEK on sanitize

7 Sufficient random entropy

⁸ No wear leveling related issues

⁹ No DEVSLP related issues







- Most SEDs have severe weaknesses
- Best case scenario: security guarantees are equivalent to software FDE

Drive		2	2		6	6	7			Imnact
Crucial MX100	×	×	×				-	u		X Compromised
Crucial MX200 (all form factors)	×	×	×							X Compromised
Crucial MX300 (all form factors)	1	1	1		×	 Image: A set of the set of the	×	 Image: A second s	 Image: A second s	✗ Compromised
Samsung 840 EVO (SATA)	×	1	1		1	1	1	×	1	~ Depends
Samsung 850 EVO (SATA)	×	1	1		1	1	1	1	1	~ Depends
Samsung T3 (USB)				×						X Compromised
Samsung T5 (USB)				×						X Compromised

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(all form factors)										
Samsung 840	×	1	1		1	1	1	×	1	~ Depends
EVO (SATA)										
Samsung 850	×	1	1		1	1	1	1	1	~ Depends
EVO (SATA)										
Samsung T3				×						Compromised
(USB)										
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- Most SEDs have severe weaknesses
- Best case scenario: security guarantees are equivalent to software FDE
- Worst case: confidentiality relies on an **if-statement**
- TCG Opal is terrible
 - Over-engineered
 - · Security goals not clear
 - No reference implementation exists
 - Implementation is not even part of complience tests
 - Structural changes needed

Drive	1	2	3	4	5	6	7	8	9	Impact
Crucial MX100	×	×	×							Compromised
(all form factors)										
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EVU (SATA)										* Commentered
(LICD)				11						▲ compromised
Samsung T5				× I						¥ Compromised
(LISR)				<u>^</u> ا						A compromised
(030)										

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Questions

See the draft paper 'Self-Encrypting Deception' Carlo Meijer

- 🖻 c.meijer@cs.ru.nl
- https://cs.ru.nl/~cmeijer/
- https://midnightbluelabs.com/



